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| OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS | | | |
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| Examiner Initials [*] | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | A | "Access and Alignment of Data in an Array Processor", by Lawrie, IEEE Trans. on Computers, Vol. c-24, No. 12, December, 1975 pgs. 1145-1155 | |
| | B | "The Knockout Switch: A Simple, Modular Architecture for High-Performance Packet Switching", by Yeh et. al, IEEE Journal on Selected Comm, Vol. SAC-5, No.8, October 1987, pgs. 274-282 | |
| | C | "Optimal, Multi-Stage Interconnection by Divide-and-Conquer Networks", by Li, Proc. of Second IASTED Conf, December 1998, pgs. 318-323 | |
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